

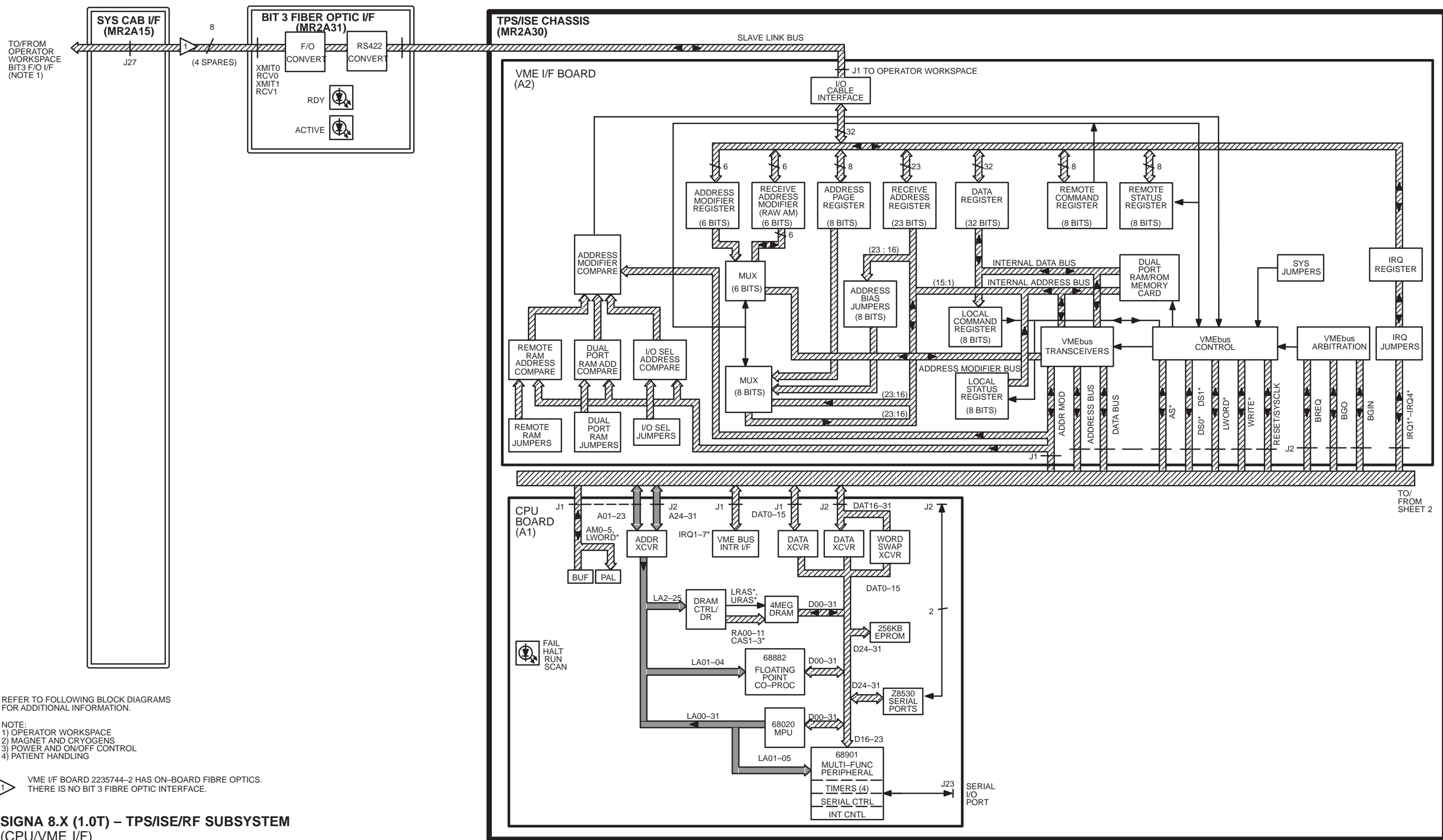
TPS/ISE (1.0T)

TABLE OF CONTENTS

<u>SECTION</u>	<u>PAGE</u>
SECTION 1 – TPS/ISE (1.0T) BLOCK DIAGRAMS	1-1
SECTION 2 – RF SUPPLEMENTAL SCHEMATICS (1.0T)	
BODY T/R SWITCH/SPLITTER SCHEMATIC (46-321332S)	2-1
DIRECT DRIVE DISABLE SCHEMATIC (46-321334S)	2-2
HEAD T/R SWITCH SCHEMATIC (46-321336S)	2-3
HEAD & BODY PREAMP SCHEMATIC (46-321338S)	2-4

**REFER TO TPS/ISE (1.5T) TAB FOR THE FOLLOWING SCHEMATICS
(SAME FOR 1.5T & 1.0T):**

- DUAL DIRECTIONAL COUPLER SCHEMATIC (46-264288S)
- DYNAMIC DISABLE FILTER MODULE SCHEMATIC (46-264776S)

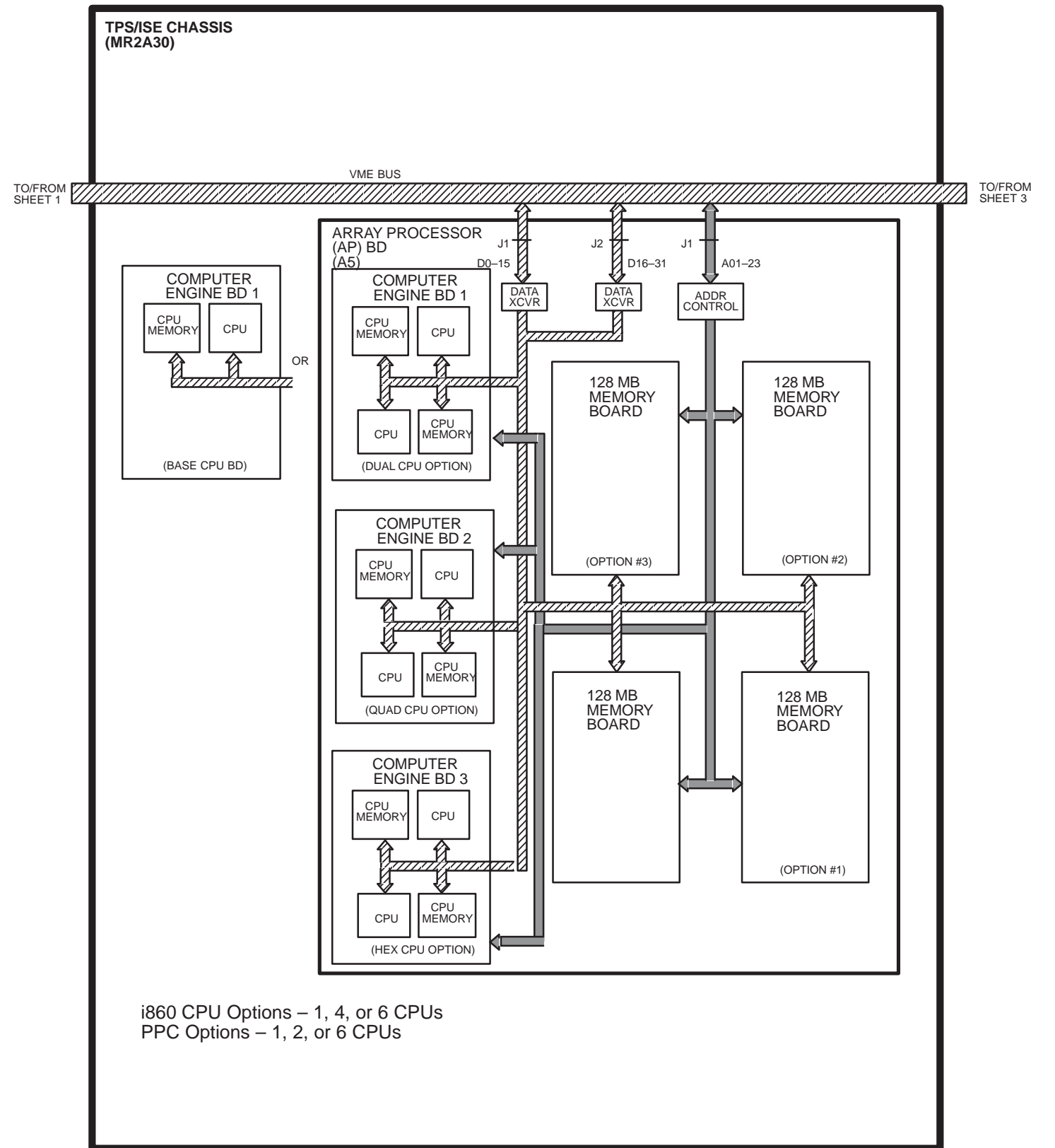


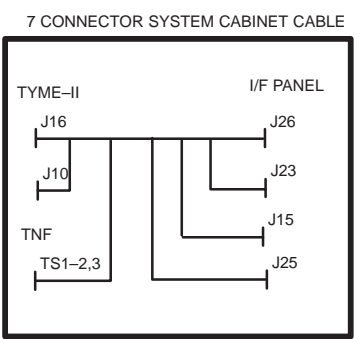
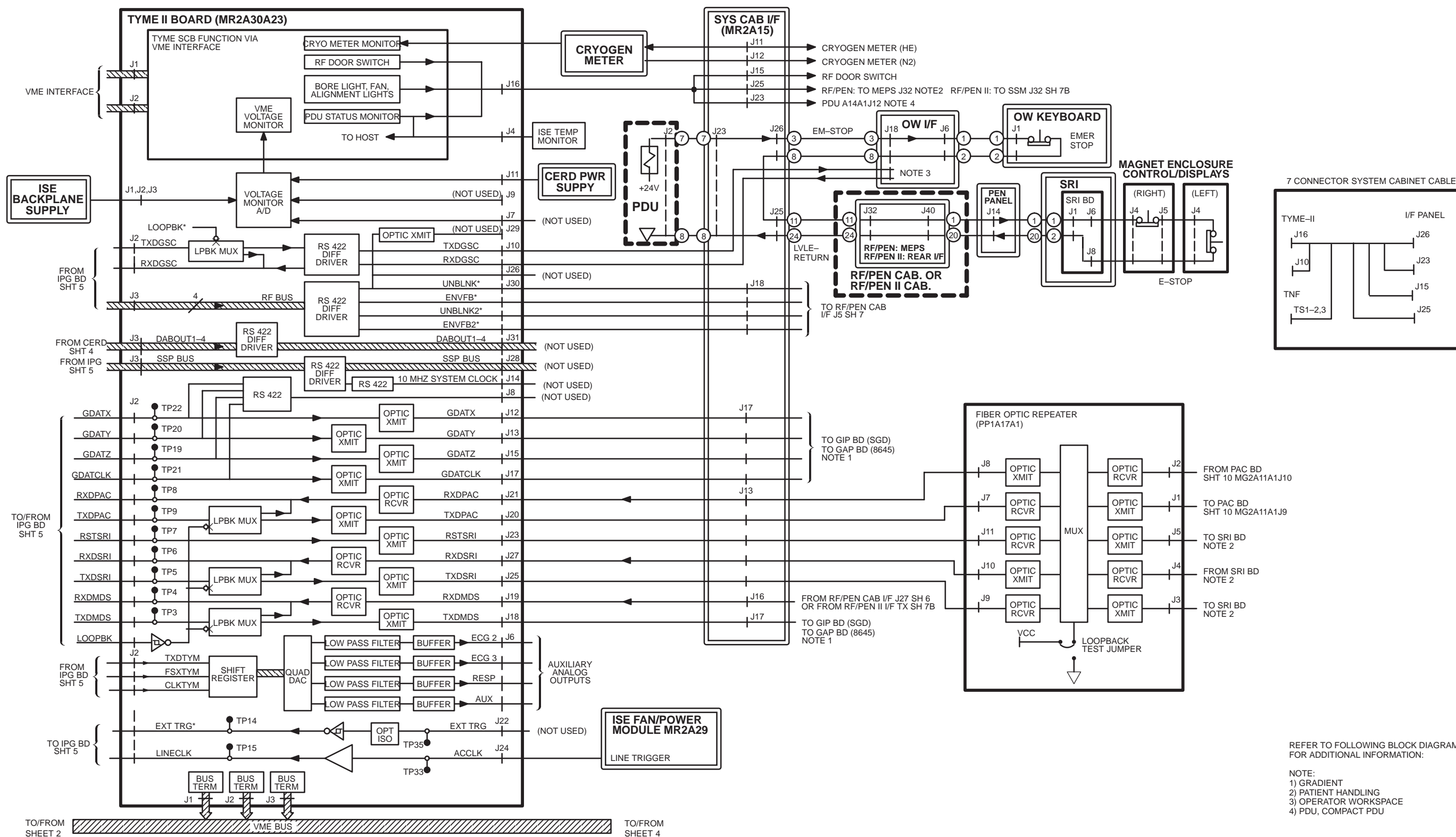
REFER TO FOLLOWING BLOCK DIAGRAMS FOR ADDITIONAL INFORMATION.

- NOTE:
- 1) OPERATOR WORKSPACE
 - 2) MAGNET AND CRYOGENS
 - 3) POWER AND ON/OFF CONTROL
 - 4) PATIENT HANDLING

1 VME I/F BOARD 2235744-2 HAS ON-BOARD FIBRE OPTICS. THERE IS NO BIT 3 FIBRE OPTIC INTERFACE.

SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM (CPU/VME I/F)

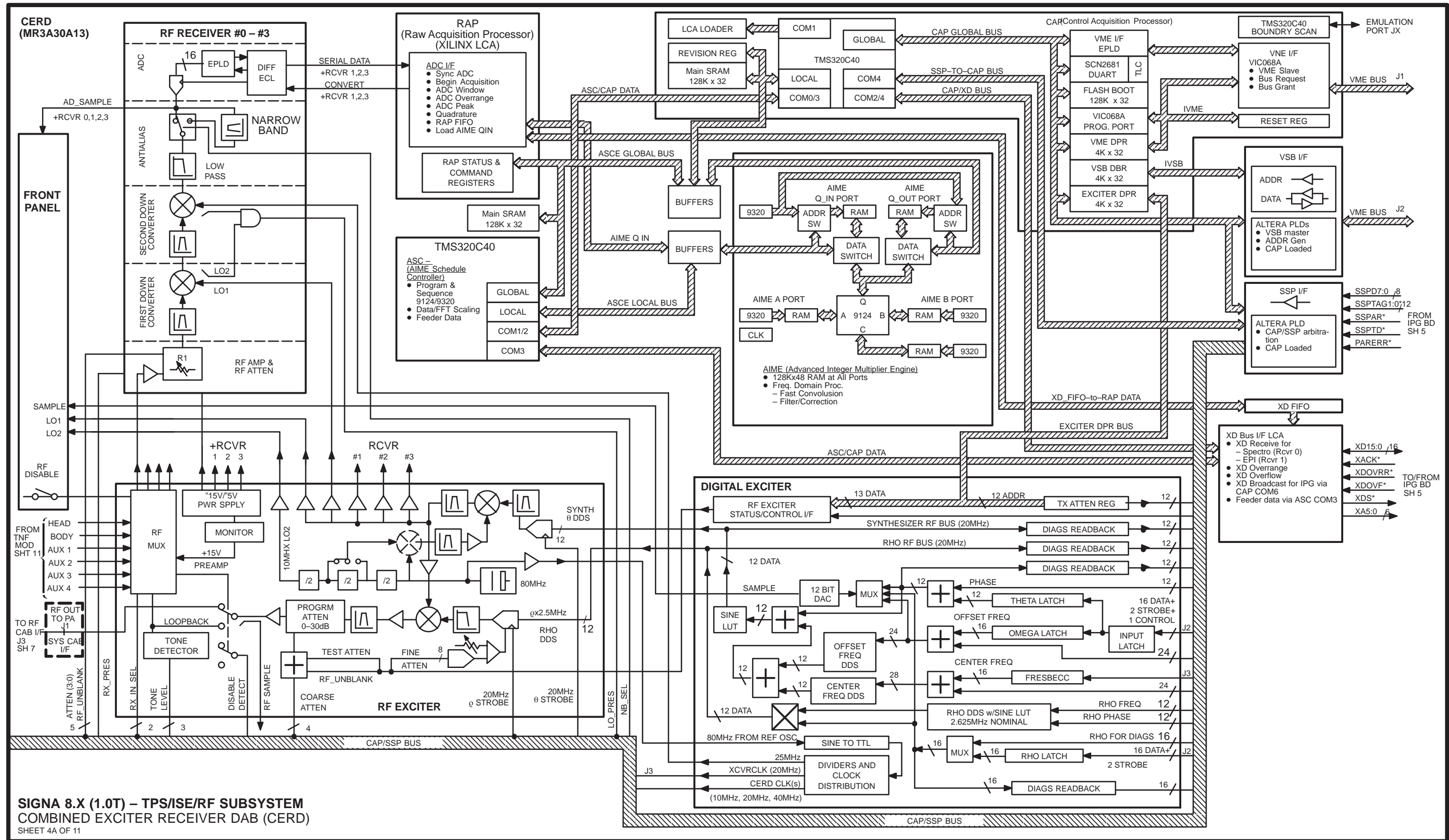


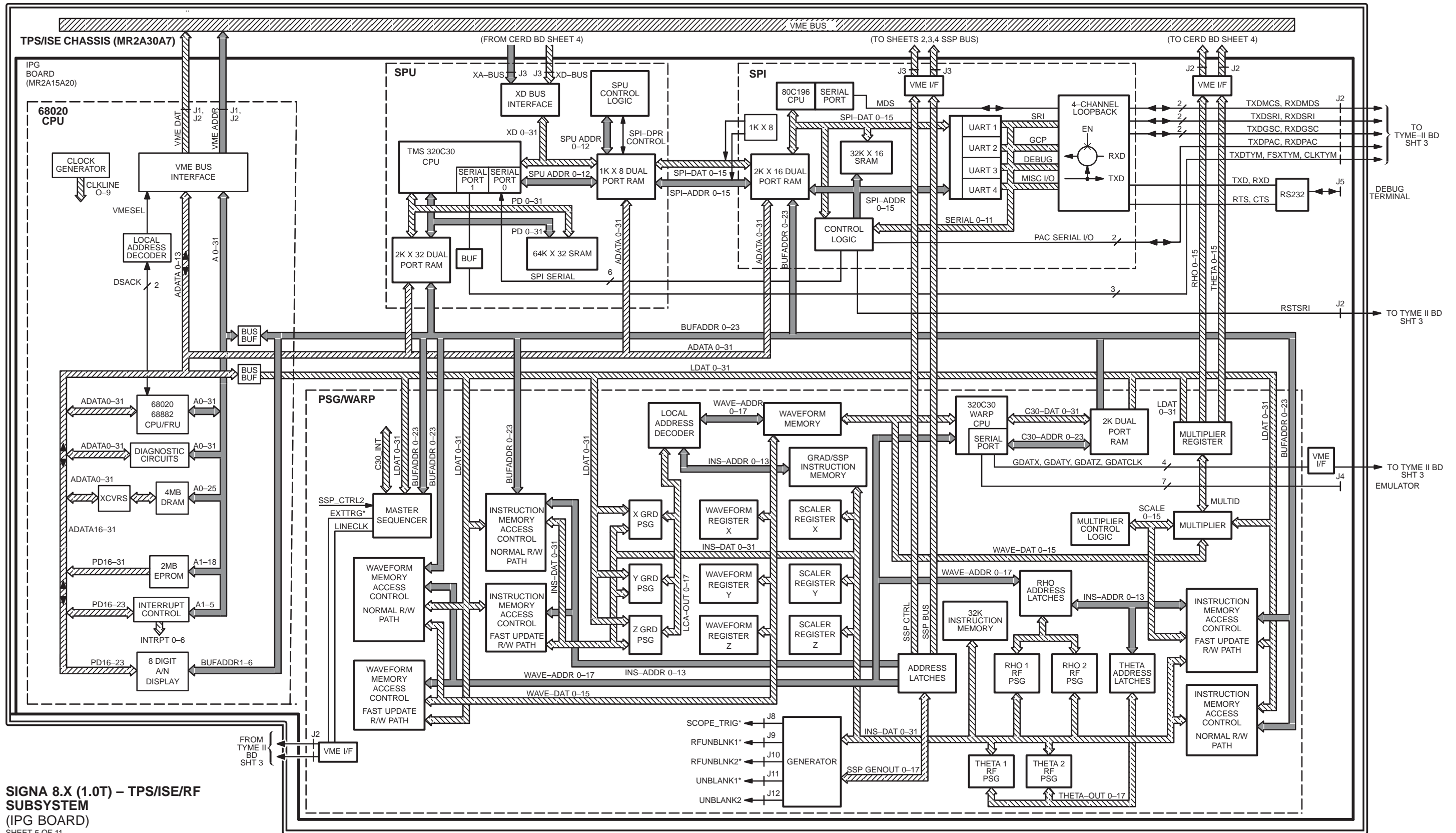


REFER TO FOLLOWING BLOCK DIAGRAMS FOR ADDITIONAL INFORMATION:

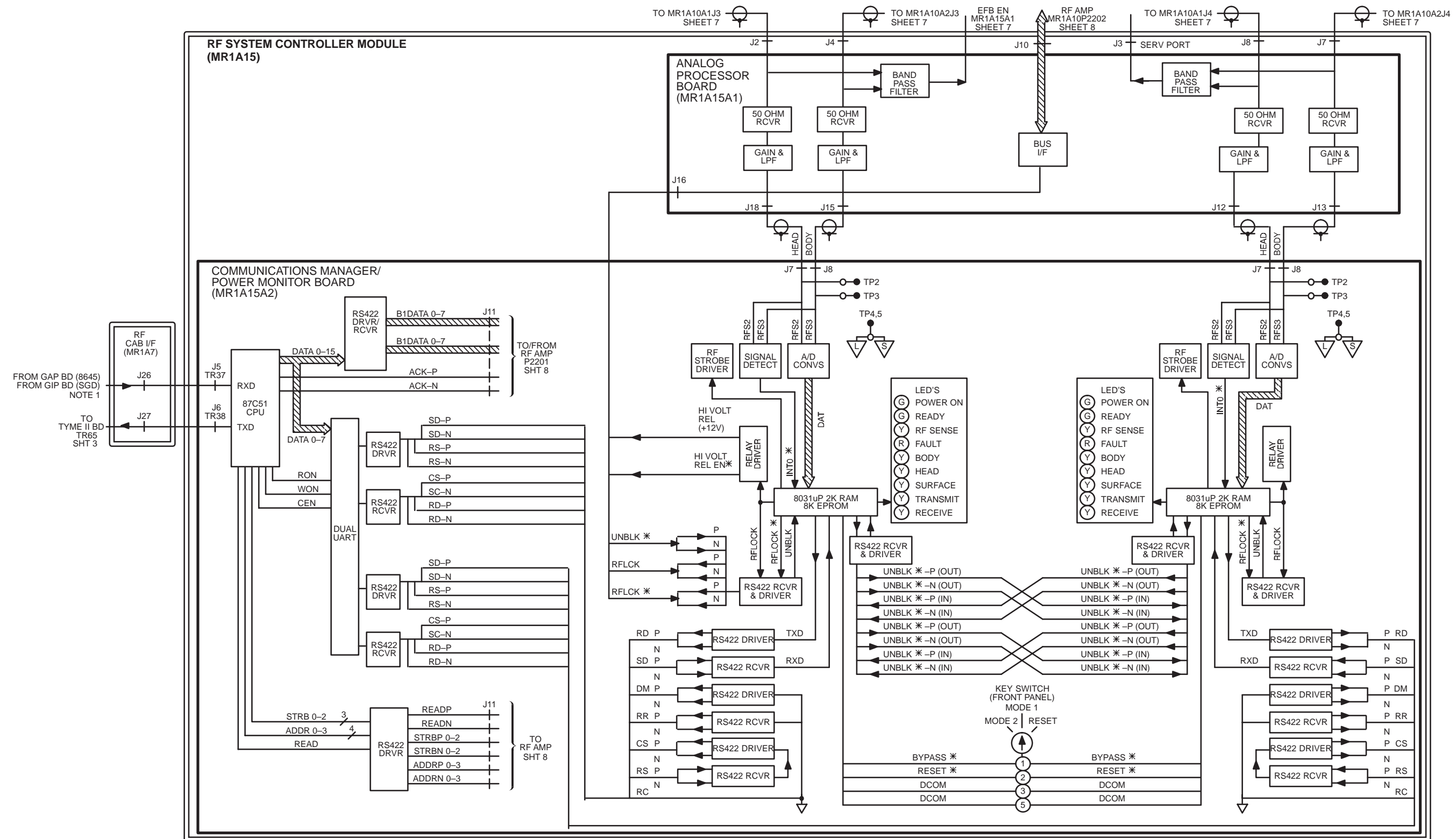
NOTE:
 1) GRADIENT
 2) PATIENT HANDLING
 3) OPERATOR WORKSPACE
 4) PDU, COMPACT PDU

SIGNA 8.X (1.0T) - TPS/ISE/RF SUBSYSTEM
(TYME II)
SHEET 3 OF 11





SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM
 (IPG BOARD)
 SHEET 5 OF 11



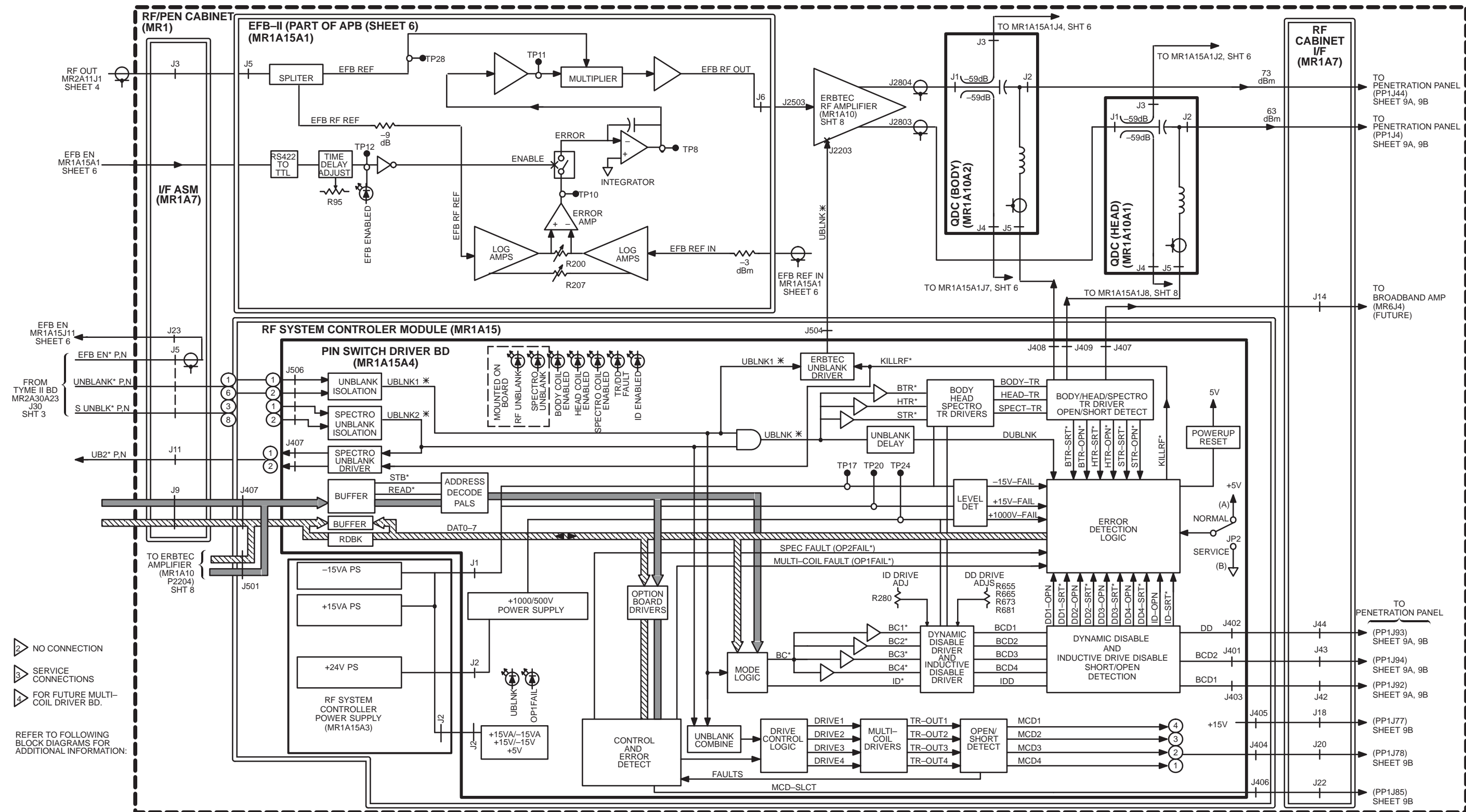
SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM
(RF/PEN CAB – COMMUNICATIONS MANAGER/POWER MONITOR)

SHEET 6A OF 11

REFER TO THE FOLLOWING FUNCTIONAL BLOCK
DIAGRAMS FOR ADDITIONAL INFORMATION:
NOTES:

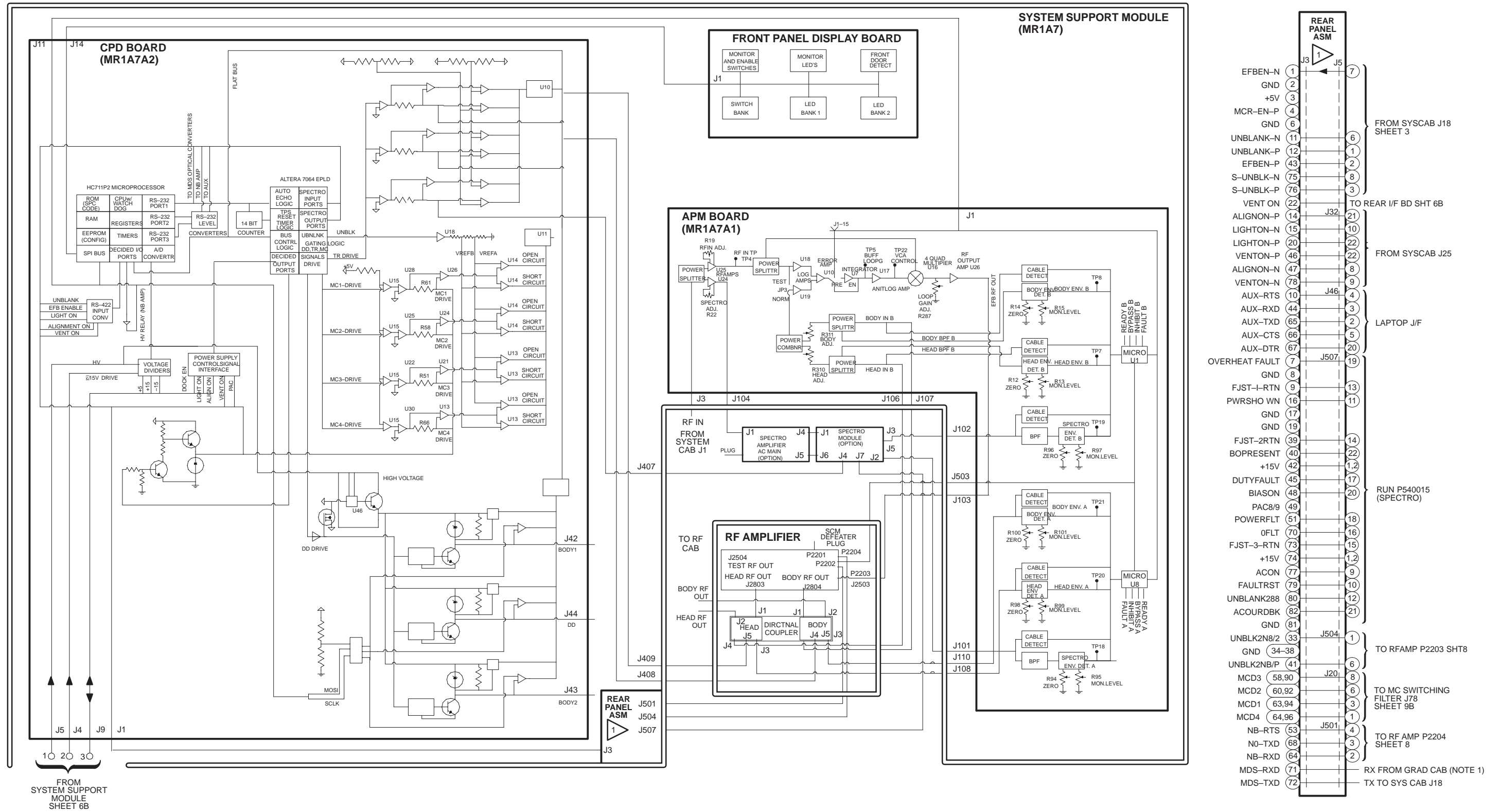
1. GRADIENT

M3081B



SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM
 (RF/PEN CAB – ENVELOPE FEEDBACK-II, PIN SWITCH DRIVER BOARD)
 SHEET 7A OF 11

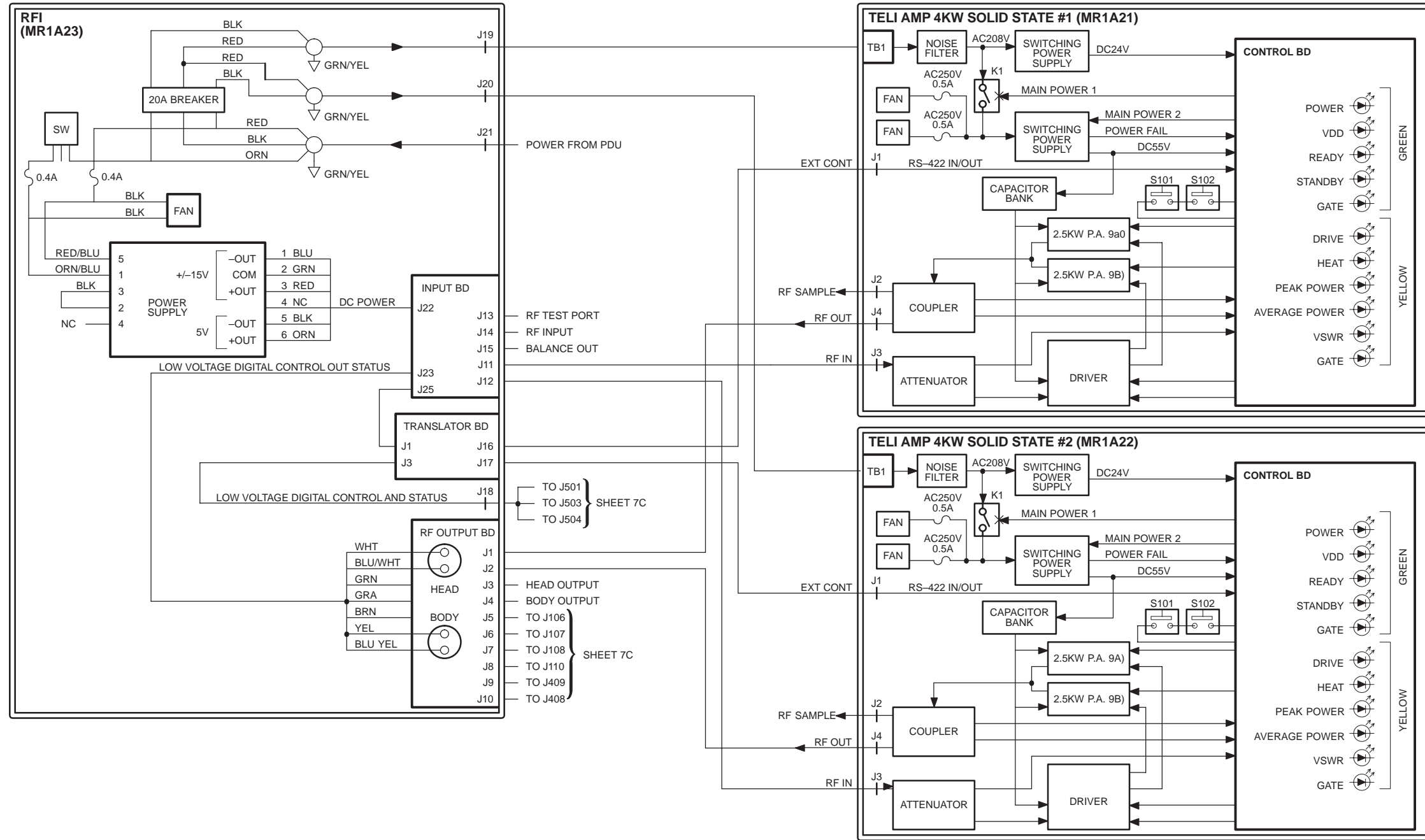
M3081B



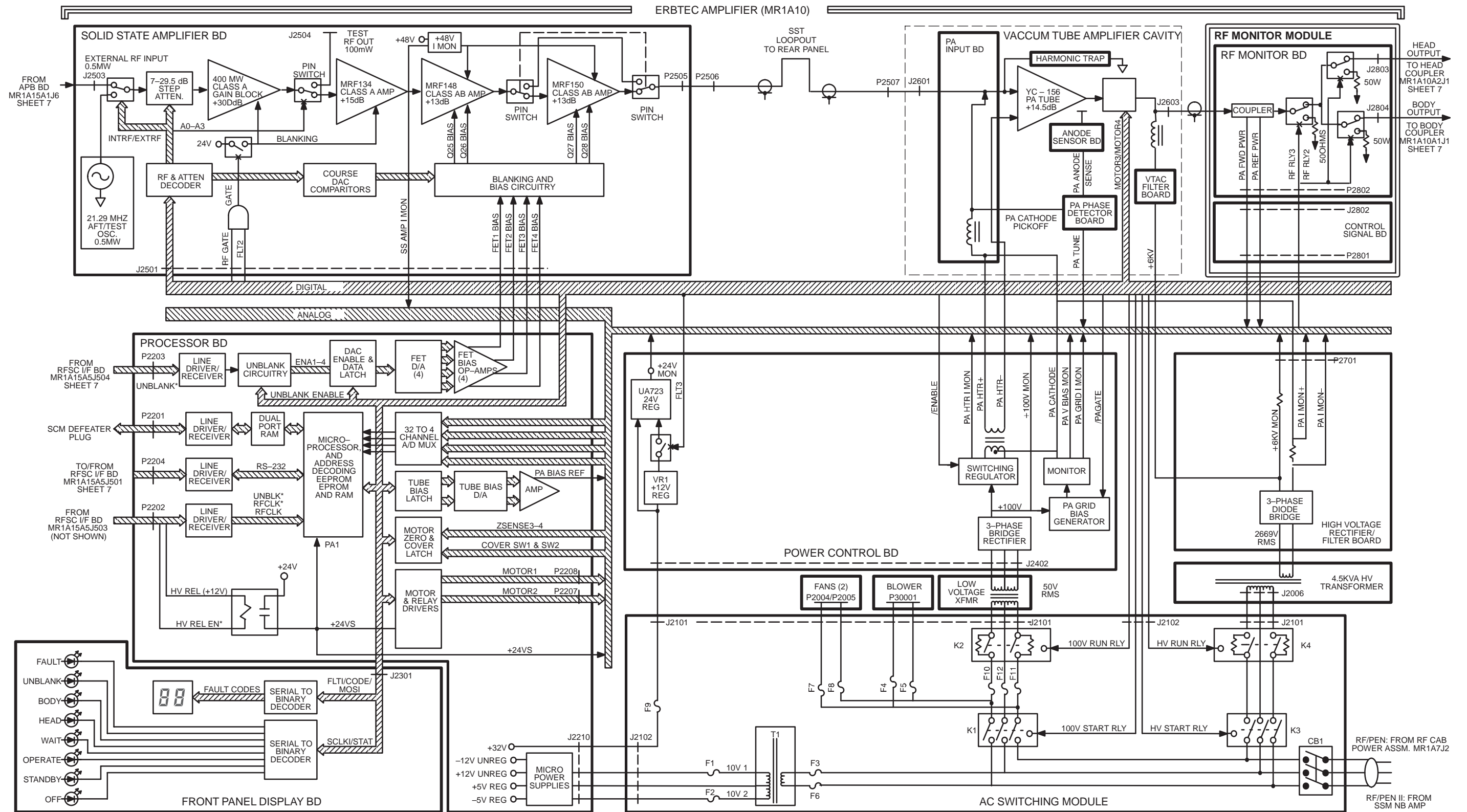
SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM
(RF PEN II CABINET – SYSTEM SUPPORT MODULE)
SHEET 7B OF 11

REFER TO THE FOLLOWING FUNCTIONAL BLOCK DIAGRAM FOR ADDITIONAL INFORMATION:

NOTE:
1) GRADIENT

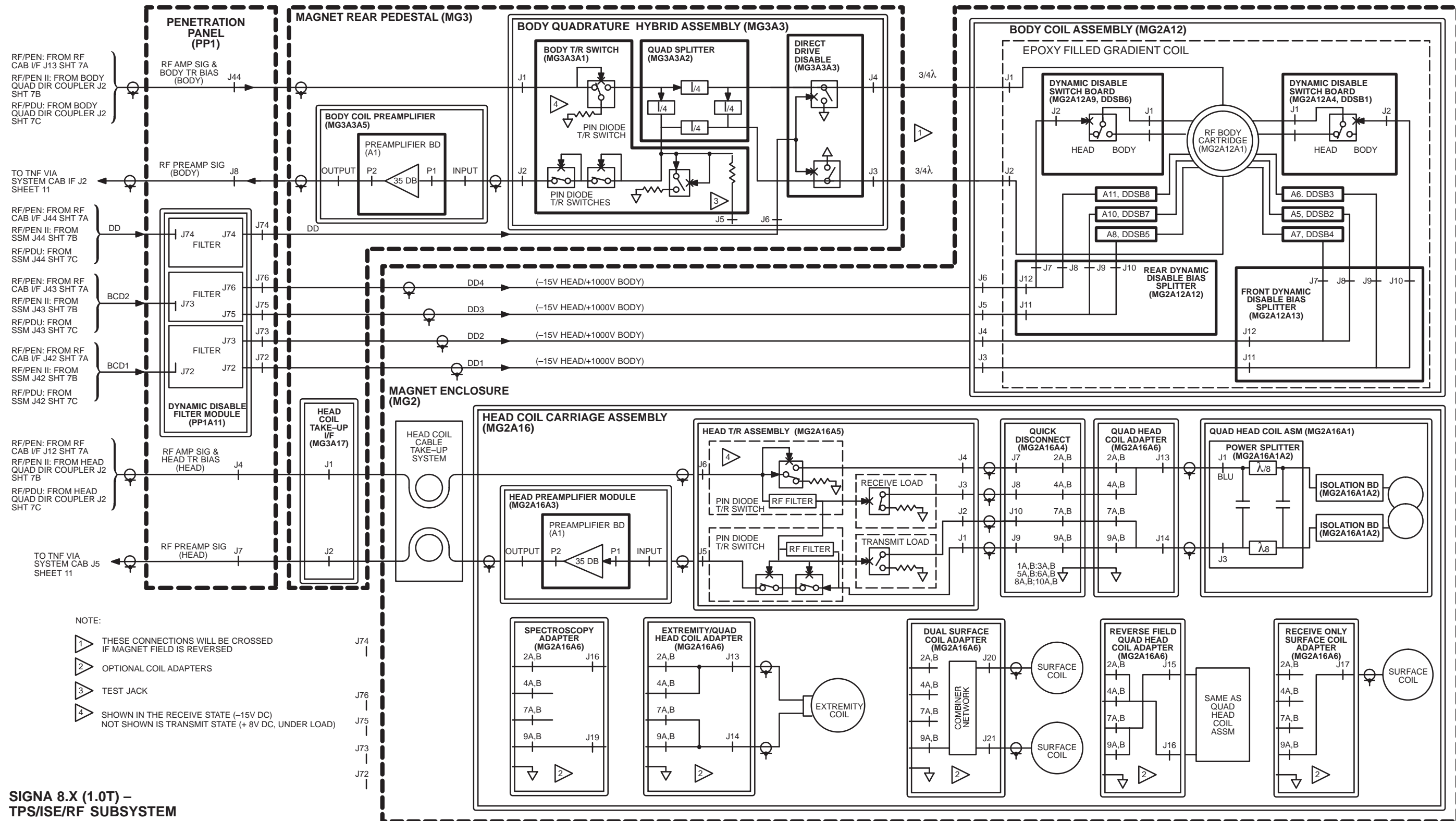


SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM
 (RF/PDU CABINET – RFI SOLID STATE AMPS)
 SHEET 7C OF 11

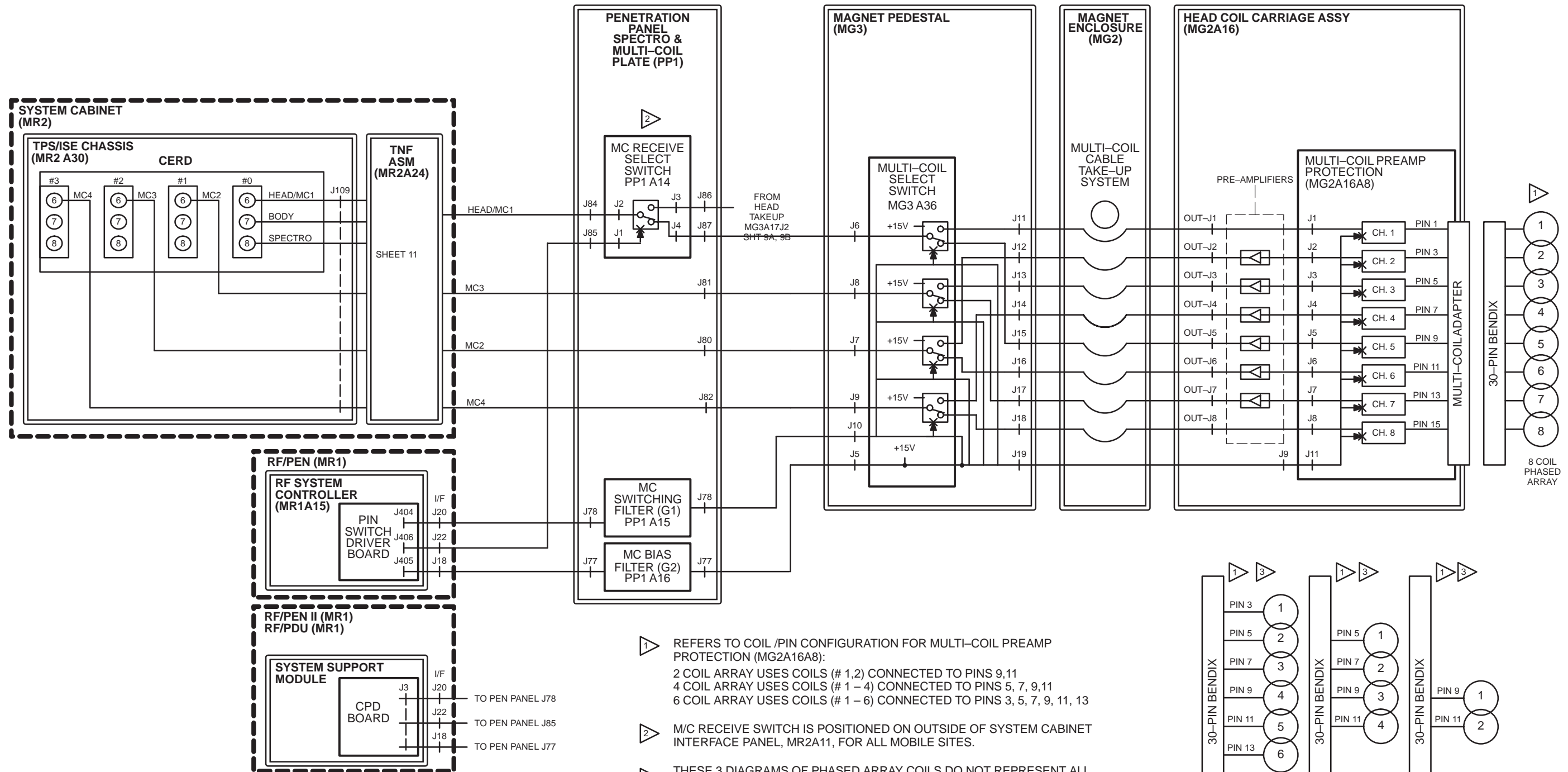


SIGNA 8.X (1.0T) - TPS/ISE/RF SUBSYSTEM
(ERBTEC 8KW RF AMPLIFIER)
SHEET 8 OF 11

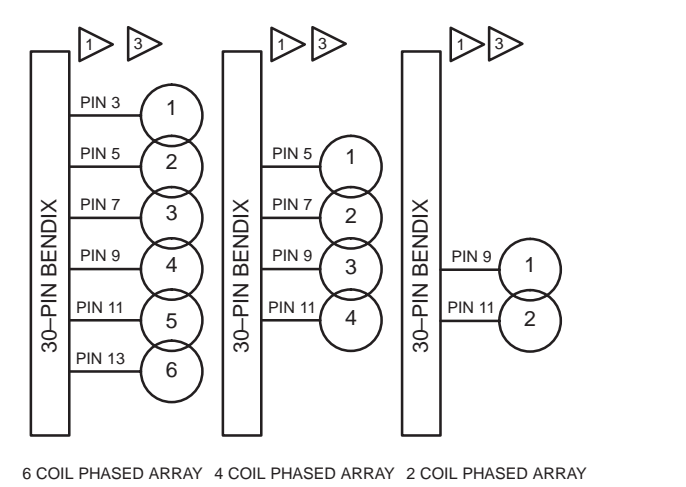
M3081B



SIGNA 8.X (1.0T) –
TPS/ISE/RF SUBSYSTEM
(MAGNET ROOM & RELATED COMPONENTS)
SHEET 9A OF 11

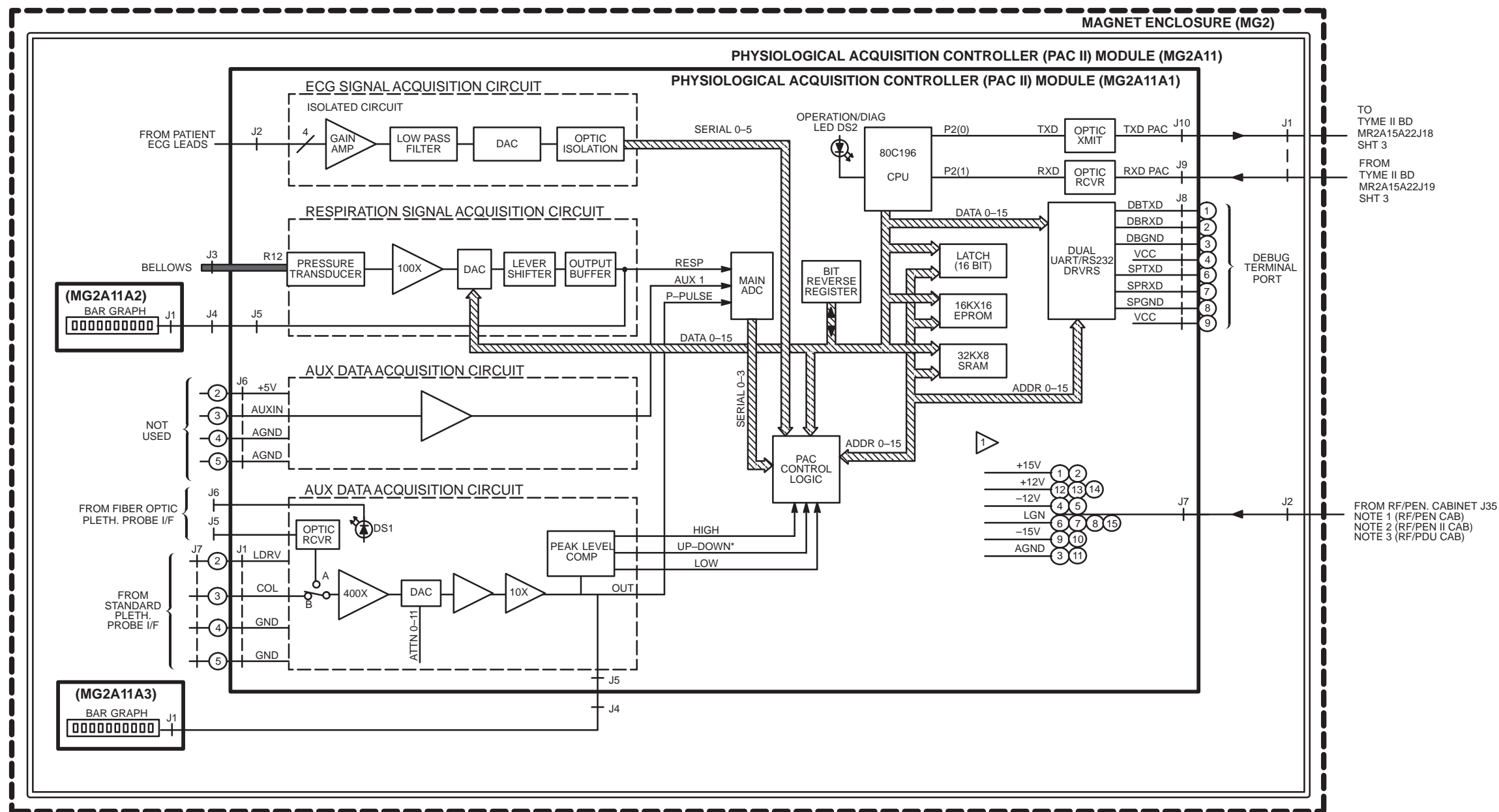


- 1 REFERS TO COIL /PIN CONFIGURATION FOR MULTI-COIL PREAMP PROTECTION (MG2A16A8):
2 COIL ARRAY USES COILS (# 1,2) CONNECTED TO PINS 9,11
4 COIL ARRAY USES COILS (# 1 - 4) CONNECTED TO PINS 5, 7, 9,11
6 COIL ARRAY USES COILS (# 1 - 6) CONNECTED TO PINS 3, 5, 7, 9, 11, 13
- 2 M/C RECEIVE SWITCH IS POSITIONED ON OUTSIDE OF SYSTEM CABINET INTERFACE PANEL, MR2A11, FOR ALL MOBILE SITES.
- 3 THESE 3 DIAGRAMS OF PHASED ARRAY COILS DO NOT REPRESENT ALL POSSIBILITIES. ALL SURFACE COILS HAVE UNIQUE SERVICE DOCUMENTATION WHICH SHOULD BE REFERRED TO FOR TROUBLESHOOTING. SEE APPLICABLE SERVICE COIL VENDOR MANUAL.



SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM
(MULTI-COIL MODULES)
SHEET 9B OF 11

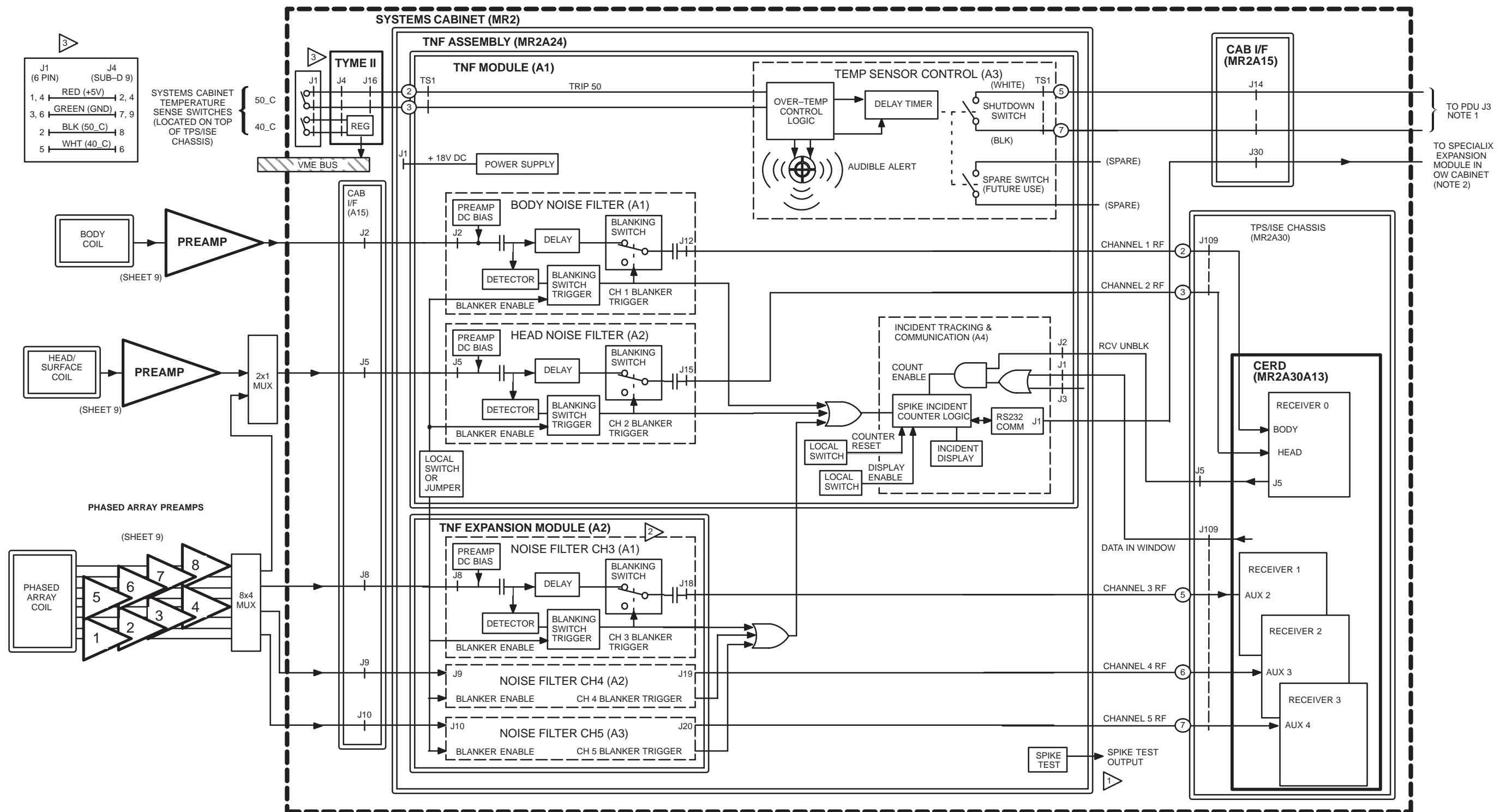
M4084B



REFER TO THE FOLLOWING FUNCTIONAL BLOCK DIAGRAM FOR ADDITIONAL INFORMATION.

NOTE:
1) PATIENT HANDLING
2) TPS/ISE/RF

1 MULTIPLE WIRES/PINS WITH SAME SIGNAL ARE NOT SHOWN



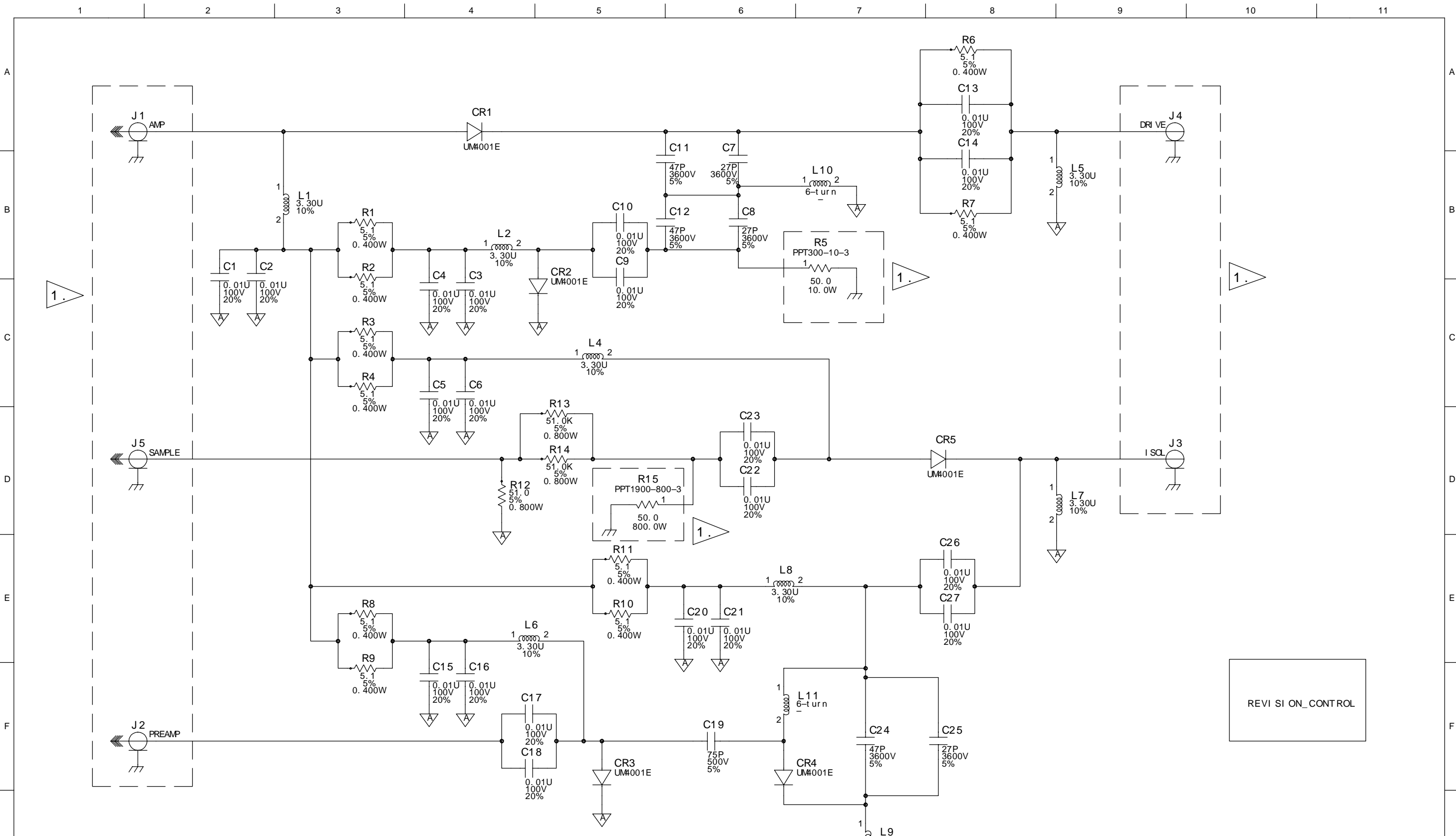
SIGNA 8.X (1.0T) – TPS/ISE/RF SUBSYSTEM
(TRANSIENT NOISE FILTER)

SHEET 11 of 11

REFER TO THE FOLLOWING FUNCTIONAL BLOCK DIAGRAM FOR ADDITIONAL INFORMATION.

NOTE:
1) PDU, COMPACT PDU
2) OPERATOR WORKSPACE

- 1 CONNECT TO ANY DESIRED CHANNEL INPUT VIA COAX FOR TEST PERIOD ONLY
- 2 HARDWARE PRESENT ONLY WITH PHASED ARRAY OPTION.



BLOCK PATHNAME		/ user / body_hyb / tr_switch_1t SHEET 1 OF 1		
REV 1	1.0T T/R BOARD		LOCATION CODE	MG3-A3-A1
DRAWING NO.	FIRST MADE FOR	DATE	ISSUED	DATE
46-321332-S	MRPVWB	1/ APR/ 93	DANIEL SMALL	20/ APR/ 93
SHT. NO. 2	EDNT. PN	MADE BY	DATE	PRINTS TO
		DAVID LYLE		740

REVISIONS	
1	DLW 27AUG93
	PCN 185808

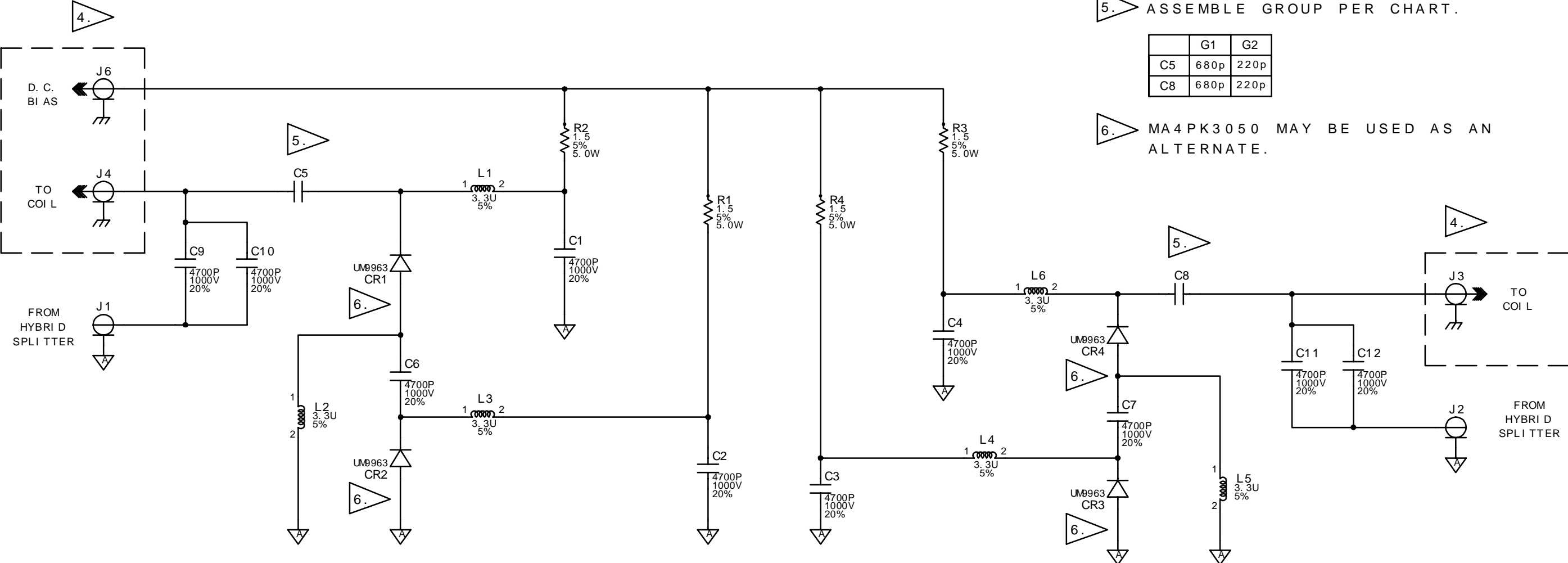
1. THE FOLLOWING CONNECTIONS ARE MADE ON THE NEXT HIGHER ASSEMBLY.

NOTES:

1. THE GROUP NUMBER AND GROUP REVISION FOR THIS PWA IS 46-321334G1-C AND 46-321334G2-B.
2. THIS SCHEMATIC MEETS THE SPECIFICATIONS OF GE SCHEMATIC STANDARDS 46-017486.
3. UNLESS OTHERWISE SPECIFIED: RESISTORS ARE IN OHMS. CAPACITORS ARE IN FARADS. INDUCTORS ARE IN HENRIES.
4. CONNECTIONS ARE MADE ON THE NEXT HIGHER ASSEMBLY.
5. ASSEMBLE GROUP PER CHART.

	G1	G2
C5	680p	220p
C8	680p	220p

6. MA4PK3050 MAY BE USED AS AN ALTERNATE.



REVISION	PCN NUMBER	DATE	SIGN OFF
0	-----	14/ APR/ 93	D. S.
1	186271	12/ OCT/ 94	D. S.
2	197730	15/ DEC/ 97	D. S.
3	197986	02/ SEP/ 98	W. S.

BLOCK PATHNAME		/ user / body_hyb / dddswit ch_1t SHEET 1 OF 1					
REV 3	DI RECT DRI VE DI SABLE SW TCH	LOCATION CODE	APPROVALS	GE MEDICAL SYSTEMS	REVISIONS		
DRAWING NO.	FIRST MADE FOR	MG2 A3 A3	DwH	MILWAUKEE WI	1 WJK 12/ OCT/ 94	3 DwH 02/ SEP/ 98	
46-321334-S	MRPWB		15/ APR/ 93		PCN# 186271	PCN# 197986	
REV. 1	MADE BY	DATE	ISSUED	DATE	PRINTS TO	740	
	D. WALK	1/ APR/ 93	D. SMALL	20/ APR/ 93			

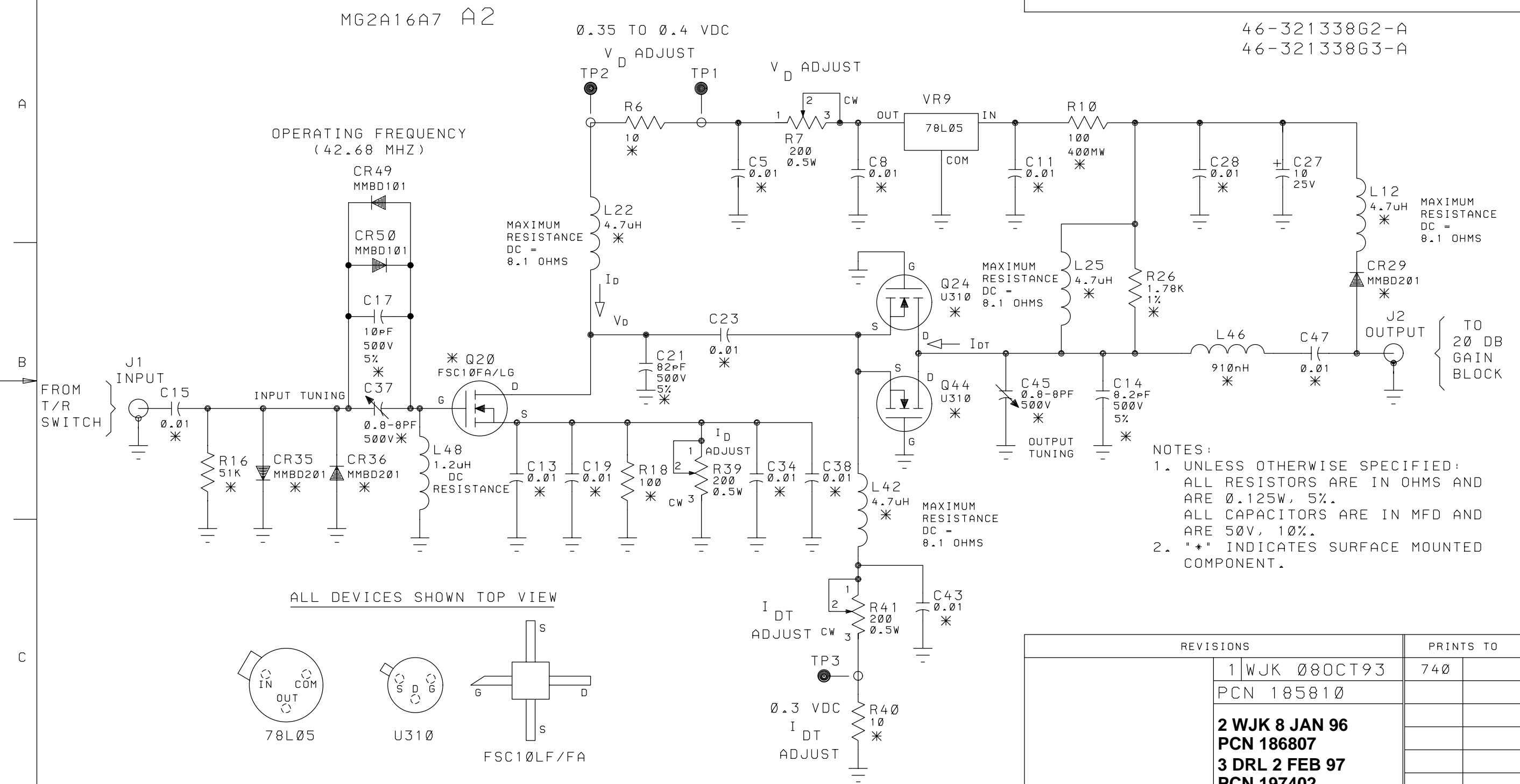
MG2A16A7 A2

46-321338G2-A
46-321338G3-A

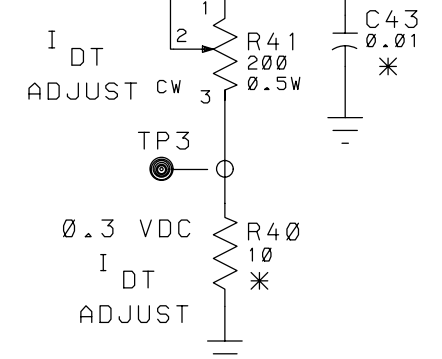
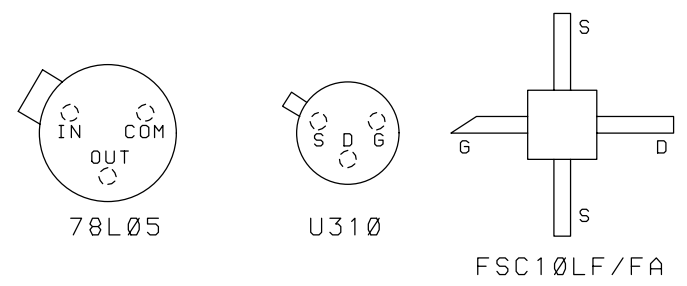
A

B

C



ALL DEVICES SHOWN TOP VIEW



REVISIONS		PRINTS TO	
1	WJK 08OCT93	740	
PCN 185810			
2 WJK 8 JAN 96			
PCN 186807			
3 DRL 2 FEB 97			
PCN 197402			

MADE BY B.KOSTOLNI 4MAR93	APPROVALS MEDICAL SYSTEMS DEPT MILWAUKEE, WI. LOCATION	46-321338-S
ISSUED MARK GRYZWA 19MAR93		CONT ON SHEET - SHT NO. 1