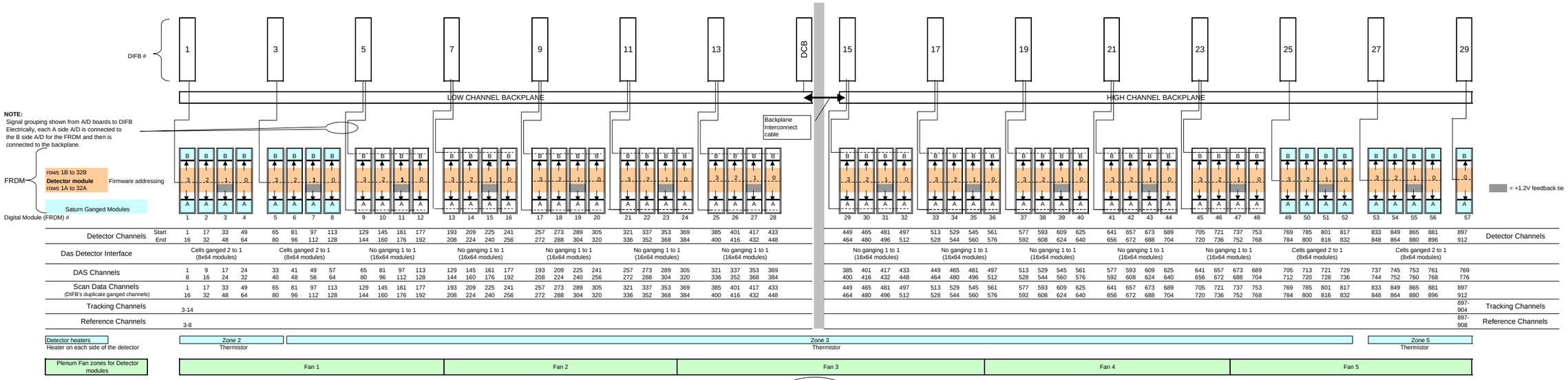
VCT HDAS_Saturn DAS/Detector Block Diagram

VCT DAS/Detector shown at 12 o'clock
Looking from the Table side of the Gantry



ISO Center

Backplane Signal Mapping (same scale as above diagram)

	LOW CHANNEL BACKPLANE		HIGH CHANNEL BACKPLANE	
CLK (from DCB)	**0** (FRDM 1 - 16, DIFB 1 - 7)	**1** (FRDM 17 - 28, DIFB 9 - 13)	**2** (FRDM 29 - 44, DIFB 15 - 21)	**3** (FRDM 45 - 57, DIFB 23 - 29)
TRIG (from DCB)	**0** (FRDM 1 - 16, DIFB 1 - 7)	**1** (FRDM 17 - 28, DIFB 9 - 13)	**2** (FRDM 29 - 44, DIFB 15 - 21)	**3** (FRDM 45 - 57, DIFB 23 - 29)
48V POWER (from fuse board)	**P48V_A (J6)** (FRDM 1 - 16, DIFB 1 - 7)	**P48V_B (J7)** (FRDM 17 - 28, DIFB 9 - 13)	**P48V_C (J8)** (FRDM 29 - 44, DIFB 15 - 21)	**P48V_D (J9)** (FRDM 45 - 57, DIFB 23 - 29)

DCB signals Clk and Trig are split into 4 outputs (0-3) for signal load reduction on the DCB.

48V input power from the 48V fuse board is split into 4 inputs (A-D) for current handling.